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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Applicant of

Joseph Fjelstad

Group Art Unit: 2811

Application Serial No. 09/020,647

Examiner: Unassigned

Filed: February 9, 1998

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For: METHOD OF MAKING A SEMICONDUCTOR CHIP

PACKAGE

Assistant Commissioner For Patents Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

It is respectfully requested that the references listed on the enclosed Form PTO-1449 be made of record and considered with respect to the above-referenced U.S. patent application. A copy of each reference is enclosed. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient mortage as First Class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, 3 (1) 2031 on July 28, 1998.

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